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<b>SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				<b>Complete if Known</b>	
				Application Number	09/605,334
				Filing Date	June 27, 2000
				First Named Inventor	GEIST, Daniel
				Art Unit	2123
				Examiner Name	Stevens, Thomas H.
Sheet	1	of	1	Attorney Docket Number	06727/000H560-USO

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	†
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				

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NON PATENT LITERATURE DOCUMENTS						
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THS	CA**	CLARK, E. Jr. et al., Model Checking, Chapter 1, MIT Press 1999, pp. 4-6				
THS	CB**	MCMILLAN, K., Symbolic Model Checking, Kluwer Academic Publishers, Chapter 2, pg. 2 and pg. 11-12, 1993, Norwell, Massachusetts				
THS	CC**	GRUMBERG, O. et al., "Model Checking and Modular Verification", ACM Transactions on Programming Languages and Systems, vol. 16, no. 3, May 1994, pp. 843-871.				
THS	CD**	CLARKE, E. et al., "Verification of the Futurebus+ Cache Coherence Protocol", Proceedings of the 11th International Conference on Computer Hardware Description Languages, October 1992, pp. 1-14				
THS	CE**	MANNA, Z. et al., "Temporal Verification of Reactive Systems - Safety", Springer-Verlag, 1995, Chapter 5, pp. 451-455				
THS	CF**	MILNER, R., "An Algebraic Definition of Simulation Between Programs", Proceedings of the 2nd International Joint Conference on Artificial Intelligence, Session No. 11, Theoretical Foundations, pp. 481-489.				
THS	CG**	BEER, I. et al., "RuleBase: Model Checking at IBM", Computer Aided Verification Conference, Haifa, July 1997, pp. 1-4.				
THS	CH**	KATZ, S. et al., "Have I written enough Properties? - A Method of Comparison between Specification and Implementation", Proceedings of the 10th IFIP WG 10.5 Advanced Research Working Conference on Correct Hardware Design and Verification Methods, p. 280-297, September 27-29, 1999.				
THS	CI**	HOSKOTE, Y. et al., "Coverage Estimation for Symbolic Model Checking", Proceedings of the Design Automation Conference DAC '99 (IEEE Computer Society Press, 1999), pp. 300-305.				
THS	CJ**	BEER, I. et al., "RuleBase: an Industry-Oriented Formal Verification Tool", Design Automation Conference DAC '96, Las Vegas, June 1996, pp. 1-6.				

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Examiner Signature	<i>Tom Stevens</i>	Date Considered	12/13/04
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